



Test Report: ELG-75-C1400

75W Constant Current Mode LED Driver

■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

Protection Function Test

Component Stress Test

■ SAFETY & E.M.C. TEST

Safety Test

E.M.C. Test

■ RELIABILITY TEST

Environment Test

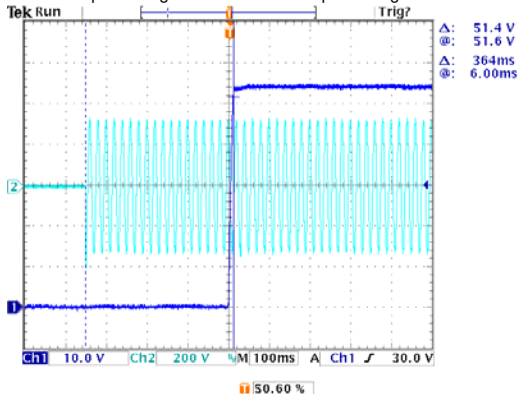
■ DESIGN VERIFY TEST

OUTPUT FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OUTPUT CURRENT ADJUST RANGE	700mA~1400mA	I/P: 230VAC O/P: LED MODE Ta: 25°C	0.336A~1.445A
2	OUTPUT CURRENT TOLERANCE	±5%	I/P: 230VAC O/P: FULL/ MIN LOAD Ta: 25°C	±1.50%
3	CONSTANT CURRENT REGION	27V~54V	I/P: 230VAC O/P: LED MODE Ta: 25°C	10V~54V
4	OPEN CIRCUIT VOLTAGE (Max)	61V	I/P: 230VAC O/P: NO LOAD Ta: 25°C	56.46V
5	OVER/UNDERSHOOT TEST	<±5%	I/P: 230VAC O/P: FULL LOAD Ta: 25°C	<5%
6	OUTPUT CURRENT RIPPLE (Max)	±5%	I/P: 230 VAC O/P: LED MODE Ta: 25°C	<5%
7	SET UP TIME(Max)	230VAC/ 500ms	I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	230VAC/ 364ms

INPUT=230VAC/50HZ @ FULL LOAD

CH1: Output Voltage CH2: AC Input Voltage



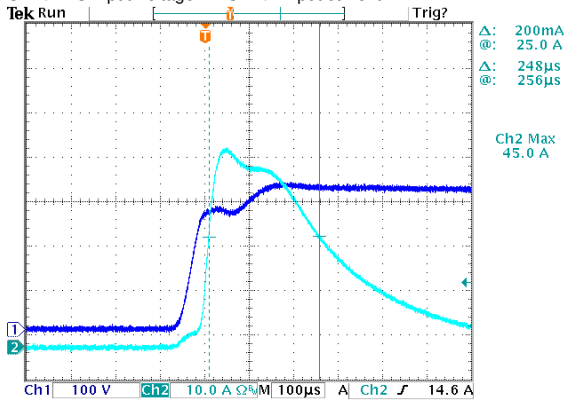
8	DIMMING TEST (For B-Type only)	SPEC:											
		<ul style="list-style-type: none"> Output constant current level can be adjusted by applying one of the three methodologies between DIM+ and DIM-: 0 ~ 10Vdc, or 10V PWM signal or resistance. Direct connecting to LEDs is suggested. It is not suitable to be used with additional drivers. Dimming source current from power supply: 100uA (typ.) DO NOT connect "DIM-" to "-V". 											
		◎ Applying additive 0 ~ 10VDC:											
		Dimming input additive voltage	0V	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V
		Output current	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%
		◎ Applying additive 10V PWM signal (frequency range 100Hz~3KHz):											
		Duty cycle of additive 10V PWM signal dimming input	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%
		Output current	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%
		◎ Applying additive resistance:											
		Dimming input additive resistance	Short	10K Ω	20K Ω	30K Ω	40K Ω	50K Ω	60K Ω	70K Ω	80K Ω	90K Ω	100K Ω
Output current	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%		
TEST RESULT:													
I/P: 230 VAC; Ta: 25°C													
1	Dimming voltage	0V	1V	2V	3V	4V	5V	6V	7V	8V	9V	10V	
	Output Current	0A	0.1045A	0.2517A	0.3889A	0.5365A	0.6797A	0.8288A	0.9767A	1.1162A	1.2626A	1.4047A	
	Percentage of rated current	0.00%	7.46%	17.98%	27.78%	38.32%	48.55%	59.20%	69.76%	79.73%	90.19%	100.34%	
	2	Dimming Duty cycle	0%	10%	20%	30%	40%	50%	60%	70%	80%	90%	100%
		Output Current	0A	0.0862A	0.2294A	0.3767A	0.5192A	0.6679A	0.8173A	0.9681A	1.1182A	1.2702A	1.4002A
		Percentage of rated current	0.00%	6.16%	16.39%	26.91%	37.09%	47.71%	58.38%	69.15%	79.87%	90.73%	100.01%
	3	Dimming Resistance	Short	10K	20K	30K	40K	50K	60K	70K	80K	90K	100K
		Output Current	0A	0.1059A	0.2502A	0.3917A	0.5350A	0.6771A	0.8178A	0.9621A	1.1064A	1.2533A	1.3966A
		Percentage of rated current	0.00%	7.56%	17.87%	27.98%	38.21%	48.36%	58.41%	68.72%	79.03%	89.52%	99.76%

INPUT FUNCTION TEST

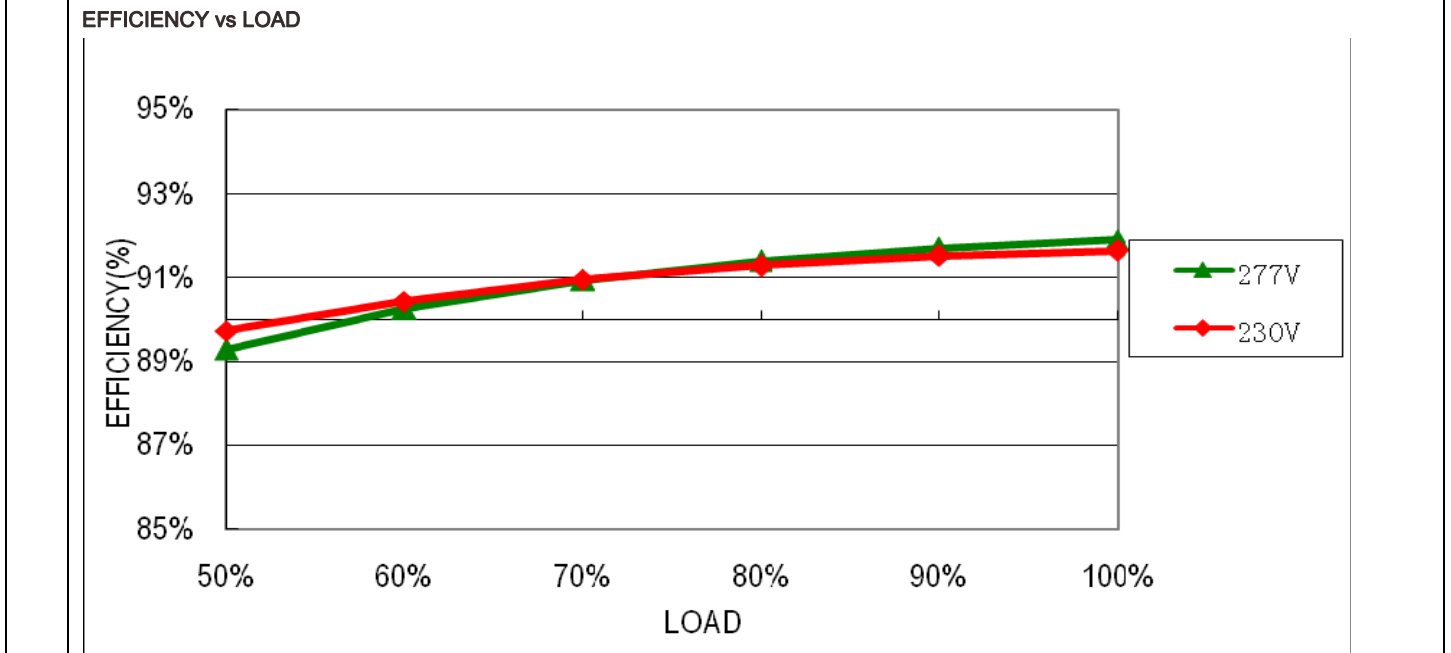
NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	INPUT VOLTAGE RANGE	100VAC~305VAC	I/P: TESTING O/P: FULL LOAD Ta: 25°C	97V~305V
			I/P: (1)LOW-LINE-3V=97 V HIGH-LINE+10V=315 V O/P: FULL/MIN LOAD ON: 30 Sec OFF: 30 Sec 10MIN (2)230VAC ON: 0.5 Sec OFF: 0.5 Sec 20MIN (POWER ON/OFF NO DAMAGE)	TEST: OK
2	INPUT FREQUENCY RANGE	47HZ ~63 HZ NO DAMAGE	I/P: 100 VAC ~305 VAC O/P: FULL~MIN LOAD Ta: 25°C	TEST: OK
3	AC CURRENT	0.38A/277VAC 0.45A/230VAC	I/P: 277 VAC I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	I =0.299A/ 277VAC I =0.354A/ 230VAC
4	LEAKAGE CURRENT	< 0.75mA / 277VAC	I/P: 277 VAC O/P: NO LOAD Ta: 25°C	L-FG: 0.349 mA N-FG: 0.389 mA
5	NO LOAD POWER CONSUMPTION	< 0.5W	I/P: 230VAC O/P: NO LOAD Ta: 25°C	0.175W/ 230VAC
6	TOTAL HARMONIC DISTORTION	Total harmonic distortion will be lower than 20% when output loading is 50% or higher at 230VAC	I/P: 230VAC O/P: 50% LOAD	THD: 12.39 %
		Total harmonic distortion will be lower than 20% when output loading is 75% or higher at 277VAC	I/P: 277VAC O/P: 75% LOAD	THD: 13.11 %
7	INRUSH CURRENT(Typ)	230V/ 50A Twidth =350 us measured at 50% Ipeak COLD START	I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	I =45A/ 230VAC Twidth =248us

INPUT=230VAC/50HZ @ FULL LOAD

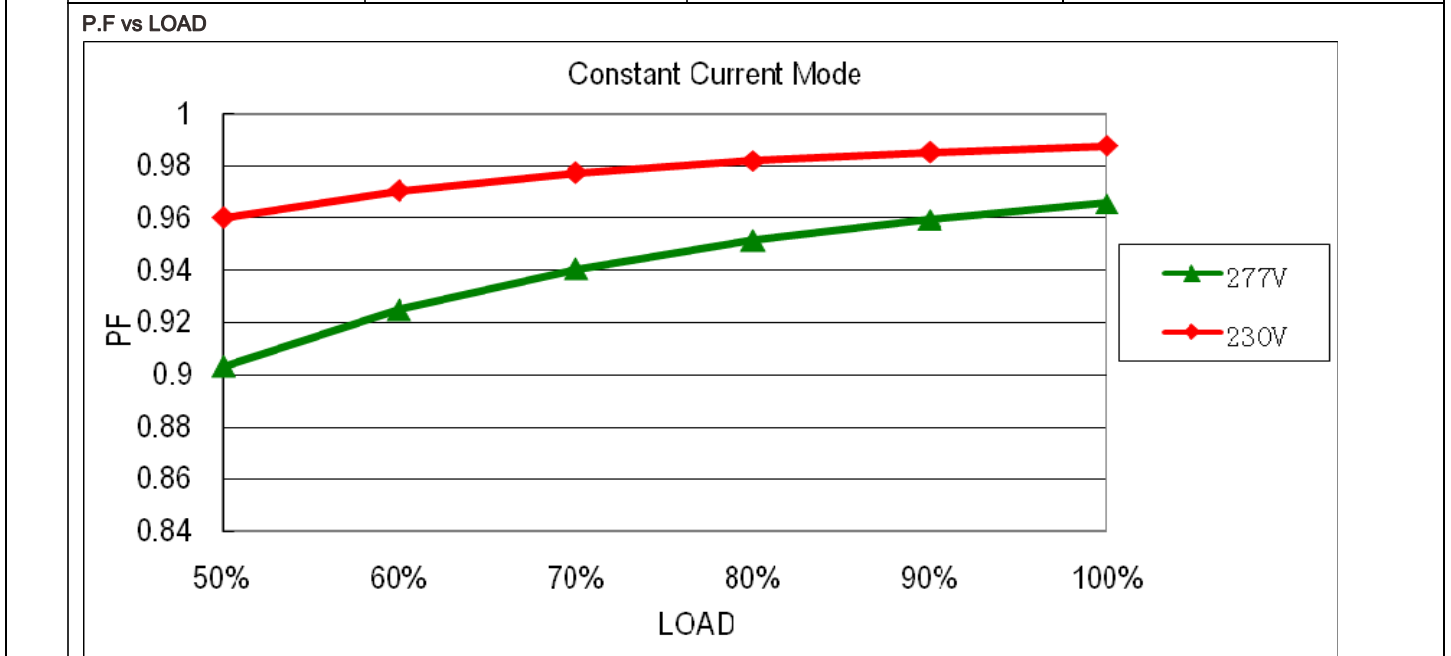
CH1: AC Input Voltage CH2: Input current



8	EFFICIENCY(Typ)	90%	I/P: 230VAC O/P: FULL LOAD Ta: 25°C	91.65%																				
	<p>EFFICIENCY vs LOAD</p> <table border="1"> <caption>Efficiency vs Load Data</caption> <thead> <tr> <th>LOAD (%)</th> <th>277V Efficiency (%)</th> <th>230V Efficiency (%)</th> </tr> </thead> <tbody> <tr> <td>50%</td> <td>~89.2</td> <td>~89.8</td> </tr> <tr> <td>60%</td> <td>~90.2</td> <td>~90.5</td> </tr> <tr> <td>70%</td> <td>~90.8</td> <td>~91.0</td> </tr> <tr> <td>80%</td> <td>~91.2</td> <td>~91.3</td> </tr> <tr> <td>90%</td> <td>~91.5</td> <td>~91.5</td> </tr> <tr> <td>100%</td> <td>~91.65</td> <td>~91.65</td> </tr> </tbody> </table>				LOAD (%)	277V Efficiency (%)	230V Efficiency (%)	50%	~89.2	~89.8	60%	~90.2	~90.5	70%	~90.8	~91.0	80%	~91.2	~91.3	90%	~91.5	~91.5	100%	~91.65
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9	POWER FACTOR	0.92/ 277VAC 0.95/ 230VAC	I/P: 277 VAC I/P: 230 VAC O/P: FULL LOAD Ta: 25°C	PF=0.966/ 277VAC PF=0.988/ 230VAC																				
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PROTECTION FUNCTION TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	OVER VOLTAGE PROTECTION	64V~79V	I/P: 100VAC I/P: 230VAC I/P: 305VAC O/P: NO LOAD Ta: 25°C	73.69V/ 100VAC 74.05V/ 230VAC 73.74V/ 305VAC Shut down o/p voltage, re-power on to recover
2	OVER TEMPERATURE PROTECTION	NO DAMAGE	I/P: 200 VAC I/P: 230VAC I/P: 305VAC O/P: FULL LOAD	O.T.P. Active Shut down o/p voltage, re-power on to recover
3	SHORT CIRCUIT PROTECTION	SHORT EVERY OUTPUT 1 HOUR NO DAMAGE	I/P: 200VAC I/P: 305VAC O/P: FULL LOAD Ta: 25°C	NO DAMAGE Hiccup mode, recovers automatically after fault condition is removed

COMPONENT STRESS TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	PWM Power Transistor	Q 2 Rated 800V/5.7A	I/P: High-Line +3V =308V O/P: (1) Full Load Turn on (2) Output Short (3) Full load continue Ta: 25°C	(1) 690V (2) 482V (3) 686V
2	O/P Diode (MOSFET)	D100 Rated 300V/20A	I/P: High-Line +3V =308V O/P: (1) Full Load Turn on (2) Output Short (3) Full load continue Ta: 25°C	(1) 174V (2) 207V (3) 171V
3	Input Capacitor	C5 Rated 47u/ 450V	I/P: High-Line +3V =308V O/P: (1) Full Load input on/off (2) Min load input on /Off (3) Full Load /Min load Change Ta: 25°C	(1) 448V (2) 445V (3) 448V
4	Control IC	U1 Rated 28V (MAX.)	I/P: High-Line +3V =308V O/P: (1) FULL LOAD (2) Output Short (3) O.V.P (4) Low Line No Load Vo(min) Ta: 25°C	(1) 17.4V (2) 15.1V (3) 17.4V (4) 15.1V
5	PFC Power Transistor	Q 1 Rated 600V/5.7A	I/P: High-Line +3V =308V O/P: (1) Full Load Turn on (2) Output Short (3) Full load continue Ta: 25°C	(1) 456V (2) 450V (3) 456V
6	Clamp Diode	D 10 Rated 800V/2A	I/P: High-Line +3V = 308V O/P: (1) Full Load input on/off (2) Output Short Ta: 25°C	(1) 668V (2) 660V

SAFETY TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	WITHSTAND VOLTAGE	I/P-O/P: 3.75KVAC/min I/P-FG : 2.0KVAC/min O/P-FG: 1.5KVAC/min	I/P-O/P: 4.2 KVAC/min I/P-FG: 2.4 KVAC/min O/P-FG: 1.8 KVAC/min Ta: 25°C	I/P-O/P: 2.388mA I/P-FG: 2.411mA O/P-FG: 1.674mA NO DAMAGE
2	ISOLATION RESISTANCE	I/P-O/P: 500VDC>100MΩ I/P-FG: 500VDC>100MΩ O/P-FG: 500VDC>100MΩ	I/P-O/P: 500 VDC I/P-FG: 500 VDC O/P-FG: 500 VDC Ta: 25°C	I/P-O/P: >9999MΩ I/P-FG: >9999MΩ O/P-FG: >9999MΩ

E.M.C TEST

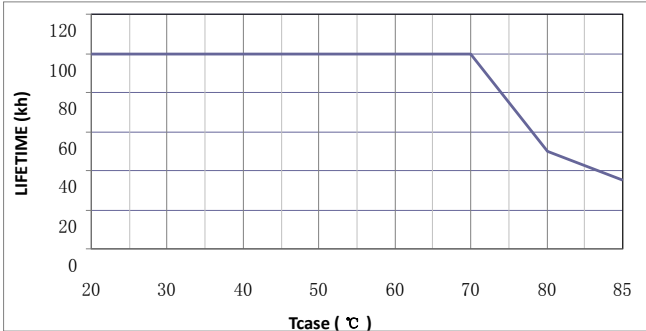
NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT
1	HARMONIC	EN61000-3-2 CLASS C	I/P: 230 VAC/50HZ O/P: FULL/50% LOAD Ta: 25°C	PASS
2	CONDUCTION	EN55015	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS Test by certified Lab
3	RADIATION	EN55015	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	PASS Test by certified Lab
4	E.S.D	EN61000-4-2 LIGHT INDUSTRY AIR: 8KV Contact: 4KV	I/P: 230 VAC/50HZ O/P: FULL LOAD Ta: 25°C	CRITERIA A
5	E.F.T	EN61000-4-4 LIGHT INDUSTRY INPUT: 1KV	I/P: 230VAC/50HZ O/P: FULL LOAD Ta: 25°C	CRITERIA A
6	SURGE	EN61000-4-5 INDUSTRY L-N: 4KV L,N-PE: 6KV	I/P: 230VAC/50HZ O/P: FULL LOAD Ta: 25°C	CRITERIA A
7	Test by certified Lab & Test Report Prepare			

■ **RELIABILITY TEST**

ENVIRONMENT TEST

NO	TEST ITEM	SPECIFICATION	TEST CONDITION	RESULT																																																																
1	TEMPERATURE RISE TEST	MODEL: ELG-75-C1400 1. ROOM AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: FULL LOAD Ta= 32.3℃ 2. HIGH AMBIENT BURN-IN: 2 HRS I/P: 230VAC O/P: FULL LOAD Ta= 64.3℃																																																																		
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2	LOW TEMPERATURE TURN ON TEST	TURN ON AFTER 2 HOUR	I/P: 305VAC/200VAC O/P: FULL LOAD Ta= -45℃	TEST: OK																																																																
3	HIGH HUMIDITY HIGH TEMPERATURE HIGH VOLTAGE TURN ON TEST	AFTER 12 HOURS IN CHAMBER ON CONTROL 60 ℃ NO DAMAGE	I/P: 305VAC O/P: FULL LOAD Ta=60 ℃ HUMIDITY= 95 %R.H	TEST: OK																																																																
4	TEMPERATURE COEFFICIENT	±0.03 %/℃(0~50℃)	I/P: 230 VAC O/P: FULL LOAD	±0.005%/℃(0~50℃)																																																																
5	STORAGE TEMPERATURE TEST	1. Thermal shock Temperature: -45℃~ +90℃ 2. Temperature change rate : 25℃ / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 5 CYCLE 5. Input/Output condition: AC OFF STATIC		TEST: OK																																																																
6	THERMAL SHOCK TEST	1. Thermal shock Temperature: -45℃~ +65℃ 2. Temperature change rate : 25℃ / MIN 3. Dwell time low and high temperature : 30 MIN/EACH 4. Total test cycle: 16 CYCLE 5. Input/Output condition: 230VAC/Full Load AC ON/OFF TEST AC on 3 sec/AC off 1 sec TEST		TEST: OK																																																																



7	VIBRATION TEST	1 Carton & 1 Set (1) Waveform: Sine Wave (2) Frequency: 10~500Hz (3) Sweep Time: 12min/sweep cycle (4) Acceleration: 5G (5) Test Time: 72min in each axis (X.Y.Z) (6) Ta: 25°C	TEST: OK																		
8	CAPACITOR LIFE CYCLE	ELG-75-C1400: SUPPOSE C102 IS THE MOST CRITICAL COMPONENT (1) I/P: 230VAC O/P: FULL LOAD Ta= 25 °C LIFE TIME (2) I/P: 230VAC O/P: FULL LOAD Ta= 60 °C LIFE TIME (3) I/P: 230VAC O/P: 75% LOAD Ta= 60 °C LIFE TIME (4) I/P: 230VAC O/P: 50% LOAD Ta= 60 °C LIFE TIME	(1) 175144 HRS (2) 20417 HRS (3) 45506 HRS (4) 67215 HRS																		
9	MTBF	MIL-HDBK-217F NOTICE 2 STRESS ANALYSIS TOTAL FAILURE RATE: 305K HRS																			
10	DMTBF/Accelerated Life Test	Demonstration Mean Time Between Failure (Expected Life): 50000 hours @ Tcase 80°C  <table border="1" data-bbox="491 817 1139 1146"> <caption>Lifetime vs Tcase Data</caption> <thead> <tr> <th>Tcase (°C)</th> <th>Lifetime (kh)</th> </tr> </thead> <tbody> <tr><td>20</td><td>110</td></tr> <tr><td>30</td><td>110</td></tr> <tr><td>40</td><td>110</td></tr> <tr><td>50</td><td>110</td></tr> <tr><td>60</td><td>110</td></tr> <tr><td>70</td><td>110</td></tr> <tr><td>80</td><td>60</td></tr> <tr><td>85</td><td>45</td></tr> </tbody> </table>		Tcase (°C)	Lifetime (kh)	20	110	30	110	40	110	50	110	60	110	70	110	80	60	85	45
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TEST RESULT	TESTER	REVIEW	APPROVAL
PASS	ZHANGZJ/ZHUOKB	SKY	LIUWY